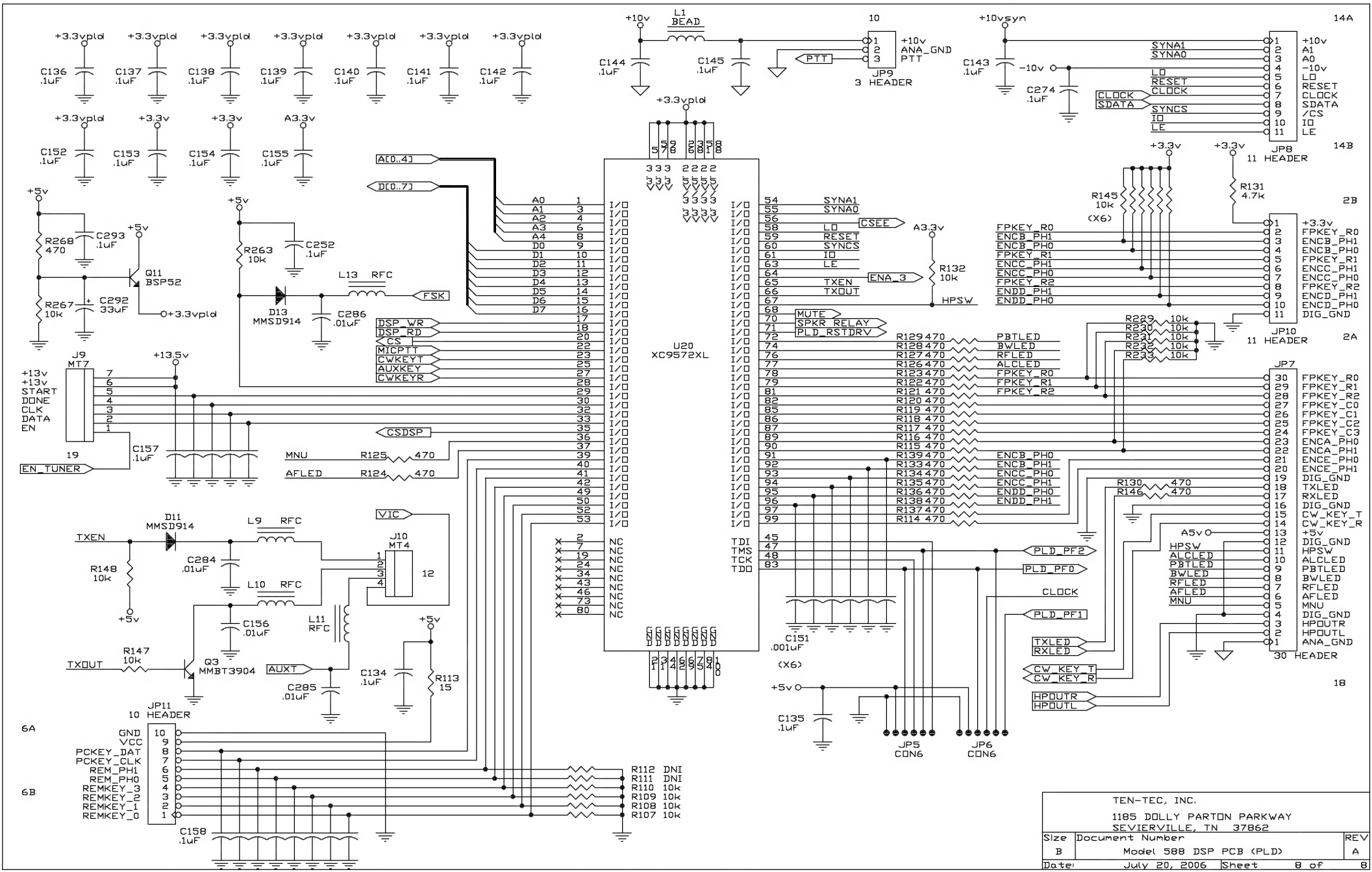
+3.3vpld
C136 .1uF
C137 .1uF
C138 .1uF
C139 .1uF
C140 .1uF
C141 .1uF
C142 .1uF
C152 .1uF
C153 .1uF
C154 .1uF
C155 .1uF
+3.3v
A3.3v
+10v
L1 BEAD
C144 .1uF
C145 .1uF
PTT
JP9 3 HEADER
+10vsyn
C143 .1uF
-10v
C274 .1uF
JP8 11 HEADER
SYNA1
SYNA0
LO
RESET
CLOCK
SDATA
SYNCS
IO
LE
CSEE
ENA_3
TXEN
TXOUT
HPSW
MUTE
SPKR_RELAY
PLD_RSTDRV
R132 10k
R131 4.7k
R145 10k (X6)
FPKEY_R0
ENCB_PH1
ENCB_PH0
FPKEY_R1
ENCC_PH1
ENCC_PH0
FPKEY_R2
ENDD_PH1
ENDD_PH0
DIG_GND
JP10 11 HEADER
R229 10k
R230 10k
R231 10k
R232 10k
R233 10k
JP7 30 HEADER
FPKEY_C0
FPKEY_C1
FPKEY_C2
FPKEY_C3
ENCA_PH0
ENCA_PH1
ENCE_PH0
ENCE_PH1
CW_KEY_T
CW_KEY_R
+5v
TXLED
RXLED
BWLED
RFLED
AFLED
MNU
HPOUTR
HPOUTL
ANA_GND
A5v
ALCLED
PBTLED
R130 470
R146 470
U20
XC9572XL
TDI
TMS
TCK
TDO
NC
C151 .001uF (X6)
C135 .1uF
JP5 CON6
JP6 CON6
PLD_PF2
PLD_PF0
PLD_PF1
CLOCK
R129 470
R128 470
R127 470
R126 470
R123 470
R122 470
R121 470
R120 470
R119 470
R118 470
R117 470
R116 470
R115 470
R139 470
R133 470
R134 470
R135 470
R136 470
R138 470
R137 470
R114 470
A[0..4]
D[0..7]
A0
A1
A2
A3
A4
D0
D1
D2
D3
D4
D5
D6
D7
DSP_WR
DSP_RD
CS
MICPTT
CWKEYT
AUXKEY
CWKEYR
CSDSP
MNU
AFLED
R125 470
R124 470
J9 MT7
+13v
+13v
START
DONE
CLK
DATA
EN
EN_TUNER
C157 .1uF
+13.5v
R268 470
C293 .1uF
Q11 BSP52
R267 10k
C292 33uF
R263 10k
C252 .1uF
D13 MMSD914
L13 RFC
FSK
C286 .01uF
D11 MMSD914
L9 RFC
TXEN
R148 10k
C284 .01uF
L10 RFC
C156 .01uF
L11 RFC
VIC
J10 MT4
12
TXOUT
R147 10k
Q3 MMBT3904
AUXT
C285 .01uF
C134 .1uF
R113 15
JP11 10 HEADER
GND
VCC
PCKEY_DAT
PCKEY_CLK
REM_PH1
REM_PH0
REMKEY_3
REMKEY_2
REMKEY_1
REMKEY_0
R112 DNI
R111 DNI
R110 10k
R109 10k
R108 10k
R107 10k
C158 .1uF
6A
6B
14A
14B
2B
2A
18
TEN-TEC, INC.
1185 DOLLY PARTON PARKWAY
SEVIERVILLE, TN 37862
Size B
Document Number
Model 588 DSP PCB (PLD)
REV A
Date: July 20, 2006
Sheet 8 of 8